

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE : NATAN VISHLITZKY, HANA MORESHET, MARTIN  
FARLEY, IZHAR SHARON AND ELIZABETH C.  
PATAPOUTIAN

SERIAL NO. : 10/692,120

FILED : October 23, 2003

FOR : METHOD FOR ENABLING OVERLAPPED INPUT/OUTPUT  
REQUESTS TO A LOGICAL DEVICE FROM MULTIPLE  
HOSTS WITH EXPLICIT ALLEGIANCES

EXAMINER : Joshua D. Schneider

ART UNIT : 2182

TERMINAL DISCLAIMER TO OBVIATE A DOUBLE PATENTING  
REJECTION OVER A PRIOR PATENT

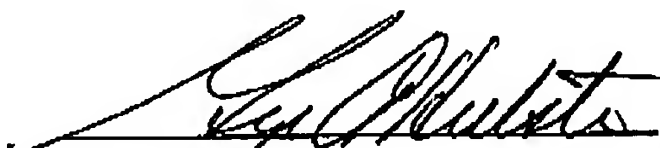
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1. ☐ For submissions on behalf of an organization (e.g., corporation, partnership, university, government agency, etc.), the undersigned is empowered to act on behalf of the organization.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

2. ☒ The undersigned is an attorney of record.

 8/8/05  
Signature Date

George A. Herbst  
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- ☒ Please charge the Terminal disclaimer fee under 37 CFR 1.20(d) to Deposit Account No. 05-0889.